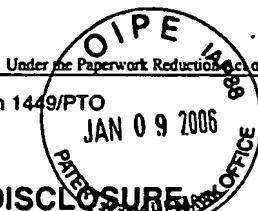


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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

**Complete If Known**

Application Number	10/675,783
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	Unknown
Art Unit	2182
Attorney Docket No.	AUS920030486US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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/JM/ AW	Mano, "Computer System Architecture", Prentice-Hall, Inc., 2 <sup>nd</sup> Ed., 1982, pp.434-443.
/JM/ AX	Inoue, "Digital mobile communication system designed for nationwide police activities - WIDE system", IEEExplore, October 1996, pp. 1-2.

Examiner: /Jason Mitchell/

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Art Unit	2182
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/JM/ AH	TANENBAUM, "Structured Computer Organization", 1984, Prentice-Hall, Inc., 2 <sup>nd</sup> Edition, pp. 10-12.
/JM/ AI	TORRELLAS et al., "False Sharing and Spatial Locality in Multiprocessor Caches", June 1994, IEEE Transactions on Computers, Vol. 43, No. 6, pp. 651-663.
/JM/ AJ	ROTHMAN et al., "Analysis of Shared Memory Misses and Reference Patterns", 2000, IEEE, pp. 187-198.

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/JM/	AG	Wikipedia "JavaServer Pages" downloaded 1/24/06 <a href="http://en.wikipedia.org/wiki/JavaServer_Pages">http://en.wikipedia.org/wiki/JavaServer_Pages</a>
/JM/	AH	"Hardware Cycle Based memory Residency", IBM, May 22, 2003, ip.com, IPCOM000012728D, pp. 1-2
/JM/	AI	HYDE, "The Art of Assembly Language", 2001, Linux Edition, pp. 247-248, retrieved Mar. 1, 2005 from <a href="http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html">http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html</a>

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		Filing Date	09/30/2003
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		Examiner Name	Unknown
		Art Unit	2182
		Attorney Docket No.	AUS920030486US1

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/JM/ AA	5051944	09-24-1991	Fetterolf et al.	
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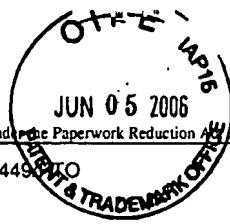
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/JM/ AL	JEONG et al., "Cost Sensitive Cache Replacement Algorithms", June 2002, Second Workshop on Caching, Coherence and Consistency, NY, pp. 1-14.

Examiner: /Jason Mitchell/

Date Considered: 05/09/2007

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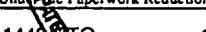
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<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 40%;">Application Number</td> <td style="width: 60%;"><b>10/675,783</b></td> </tr> <tr> <td>Filing Date</td> <td><b>09/30/2003</b></td> </tr> <tr> <td>First Named Inventor</td> <td><b>Levine et al.</b></td> </tr> <tr> <td>Examiner Name</td> <td><b>Unknown</b></td> </tr> <tr> <td>Art Unit</td> <td><b>2182</b></td> </tr> <tr> <td>Attorney Docket No.</td> <td><b>AUS920030486US1</b></td> </tr> </table>		Application Number	<b>10/675,783</b>	Filing Date	<b>09/30/2003</b>	First Named Inventor	<b>Levine et al.</b>	Examiner Name	<b>Unknown</b>	Art Unit	<b>2182</b>	Attorney Docket No.	<b>AUS920030486US1</b>
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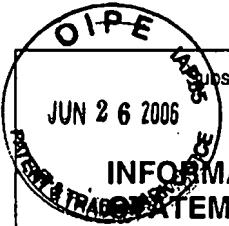
Date Considered: 05/09/2007

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Complete If Known	
Application Number	10/675,783
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First Named Inventor	Levine et al.
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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>( Not for submission under 37 CFR 1.99)</i>	Application Number		10675783	
	Filing Date		2003-09-30	
	First Named Inventor		Levine et al.	
	Art Unit		2182	
	Examiner Name		Unknown	
	Attorney Docket Number		AUS920030486US1	

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Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
/JM/	1	6944720	B2	2005-09-13	Sperber et al.	
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/JM/	1	20040030870	A1	2004-02-12	Buser	

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/JM/	1	6973417	B1	2005-12-06	Maxwell et al.	
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/JM/	1	5966538		1999-10-12	Granston et al.	
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/JM/	7	6594820	B1	2003-07-15	Ungar	

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/JM/	1	5761103		1998-06-02	Oakland et al.	
/JM/	2	6549959	B1	2003-04-13	Yates et al.	
/JM/	3	6772322	B1	2004-08-03	Merchant et al.	
/JM/	4	5937437		1999-08-10	Roth et al.	
/JM/	5	6070009		2000-05-30	Dean et al.	
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/JM/	7	6687794	B2	2004-02-03	Malik	
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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <small>( Not for submission under 37 CFR 1.99)</small>	Application Number		10675783
	Filing Date		2003-09-30
	First Named Inventor		Levine et al.
	Art Unit		2182
	Examiner Name		Unknown
	Attorney Docket Number		AUS920030486US1

/JM/	9	6973542	B1	2005-12-06	Schmuck et al.	
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/JM/	1	"Method for the dynamic prediction of nonsequential memory accesses", September 25, 2002, pp. 1-4, ip.com IPCOM000009888D	<input type="checkbox"/>
/JM/	2	"Cache Miss Director - A Means of Prefetching Cache Missed Lines", August 1, 1982, IBM Technical Disclosure Bulletin, Vol. 25, Issue 3A, pp.1286	<input type="checkbox"/>

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/JM/	2	5991708		1999-11-23	Levine et al.	
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/JM/	5	5987598		1999-11-16	Levine et al.	
/JM/	6	6009514		1999-12-18	Henzinger et al.	
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		First Named Inventor		Levine et al.	
		Art Unit		2182	
		Examiner Name		Unknown	
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/JM/	1	"CPU Cache", Wikipedia, pp. 1-14 retrieved 11/1/06 <a href="http://en.wikipedia.org/wiki/CPU_cache">http://en.wikipedia.org/wiki/CPU_cache</a>					<input type="checkbox"/>
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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <small>( Not for submission under 37 CFR 1.99)</small>	Application Number	10675783
	Filing Date	2003-09-30
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		Examiner Name		Unknown	
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	First Named Inventor	Levine et al.
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/JM/	9	6119075		2000-09-12	Dean et al.	
/JM/	10	6189141	B1	2001-02-13	Benitez et al.	
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/JM/	20	6446029	B1	2002-09-03	Davidson et al.	
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<b>RELATED PATENT APPLICATIONS</b>					
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/JM/	AB	10/674,604 / AUS920030478US1	DeWitt, Jr. et al.	Method and Apparatus for Selectively Counting Instructions and Data Accesses	Sep. 30, 2003
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				APPLICANT Levine et al.		
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